REMARKS/ARGUMENTS

The Applicants have carefully considered this application in connection with the Examiner's Action and respectfully request reconsideration of this application in view of the foregoing amendments and the following remarks. The Applicants originally submitted Claims 1-20 in the application. In response to the present office action, the Applicants have amended Claims 1, 8 and 15. Accordingly, Claims 1-20 are currently pending in the application. The Applicants respectfully request reconsideration of this application in view of the foregoing amendments and the following remarks.

I.   Formal Matters and Objections

The Examiner has objected to the drawings as being informal. The Applicant requests that the Examiner hold the objection in abeyance until such time as the Examiner indicates allowable subject matter.

The Examiner has objected to the specification as containing informalities, namely that the title of the invention is not descriptive. The Applicant has offered an amended title which is more clearly indicative of the of the invention to which the claims are directed.

The Examiner has objected to Claim 15 due to the inappropriate use of a semicolon in the claim limitation. The Applicant has amended Claim 15 to correct this inadvertent error and appreciates the Examiner's diligence in finding and bringing this error to his attention.

The Examiner has objected to Claims 4, 11 and 16 under 35 U.S.C. 112 for the use of the limitation "instructions are ungrouped," with the assertion that this limitation is unclear. The Applicant respectfully directs the Examiner's attention to Table 1, Pipeline Stages, and paragraph

29 of the specification. In Table 1, the Group (GR) stage of the pipeline is described as being employed to check grouping and dependency rules and group valid instructions. It is thus inherent in the operation of a processor comprising this pipeline to group instructions in the GR stage of the pipeline. This provides the antecedent basis for the ungrouping of instructions when the categorization logic generates instruction type information, as recited in Claims 4, 11, and 18. The Applicant respectfully requests that the Examiner remove the objection to these claims, and interpret the claim limitation according to the above description.

II.   Rejection of Claims 1-20 under 35 U.S.C. §102(b)

The Examiner rejected Claims 1-20 under 35 U.S.C. §102(b) as being anticipated by U.S. Patent 5,651,125 to Witt, et al. Witt does not anticipate these claims as amended. Independent Claims 1, 8 and 15 include the limitation that processor resources are allocated according to a predefined priority of functional units in the processor. This limitation is not taught or suggested in Witt, and thus Witt is does not teach or suggest each and every limitation of the claims. Furthermore, the claims depending from Claims 1, 8 and 15 are not anticipated, as they incorporate all the limitations of the claims from which they depend. Therefore, the Applicant respectfully requests that the Examiner remove the rejection of claims 1-20.
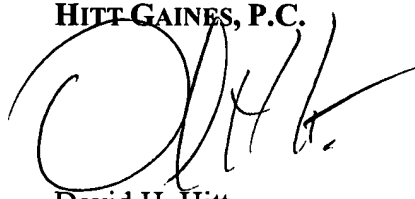
III.   Conclusion

In view of the foregoing amendments and remarks, the Applicant now sees all of the claims currently pending in this application to be in condition for allowance and therefore earnestly solicits a Notice of Allowance for Claims 1-20.

The Applicant requests the Examiner to telephone the undersigned attorney of record at (972) 480-8800 if such would further or expedite the prosecution of the present application.

Respectfully submitted,

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